Notice of References Cited Application/Control No. 10/606,161 Examiner Michael J. Simitoski Applicant(s)/Patent Under Reexamination WUIDART, SYLVIE Art Unit Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,349,318 B1	02-2002	Vanstone et al.	708/492
*	В	US-6,419,159 B1	07-2002	Odinak, Gilad	235/492
*	С	US-6,498,404 B1	12-2002	Thuringer et al.	307/32
*	D	US-6,654,884 B2	11-2003	Jaffe et al.	713/168
*	Е	US-6,658,569 B1	12-2003	Patarin et al.	713/194
*	F	US-6,804,782 B1	10-2004	Qiu et al.	713/194
*	G	US-6,839,847 B1	01-2005	Ohki et al.	713/194
*	Н	US-2002/0124178 A1	09-2002	Kocher et al.	713/193
	ı	US-			
	J ·	US-			
	к	US-			
	L	US-			
	М	US-		-	

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N			•		
	0					
	Р					
	Q					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)						
	υ	Hennessy, John L. et al. Computer Organization and Design, The Hardware/Software Interface. 1998 Morgan Kaufmann, pp. B2-B11 & B46.						
	>	National Semiconductor. "54LS157 Quad 2-Line to 1-Line Data Selectors/Multiplexers", June 1989.						
	8	ON Semiconductor. "MC14001B Series B-Suffix Series CMOS Gates", August 2000.						
	x							

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.